

Form P.T.O. 1449

U.S. Department of Commerce
Patent and Trademark OfficeDocket No.
P01,0062

Serial No.

Applicant
Robert BAUMGARTNER et al.LIST OF PRIOR ART CITED BY APPLICANT
(use several sheets if necessary)

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

| Examiner's Initials | | Document Number | Date | Name | Class | Subclass | Filing Date If appropriate |
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| chk | AA | 5,572,687 | 11/05/96 | Alnuweiri | | | |

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| | | Document Number | Date | Country | Class | Subclass | Translation | |
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| chk | AD | WO 92/21088 | 11/26/92 | PCT | | | | |
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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| chk | AI | Packer et al., "Message Passing Supports Multiple Processor Design", Computer Design/June 15, 1984, pp. 117-124. |
| chk | AJ | IEEE Standard for a High-performance Synchronous 32 Bit Bus: Multibus II. |
| chk | AK | Bussysteme, Parallele und serielle Bussysteme, lokale Netze, 1987 |
| chk | AL | 20 MHZ Multibus II Parallel System Bus Investigation. |
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Examiner

C. Kuall

Date Considered

12/2/03

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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| Form PTO-1449 | | | | | Docket No. P01,0062 | | Serial No. 09/807,352 | |
| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (use several sheets if necessary) | | | | | Robert Baumgartner et al. | | | |
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| Examiner C. Knoll | | | | Date Considered 12/2/03 | | | | |
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